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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Vu, et al.

Docket No: TI-36082

Serial No: 10/788,841

Examiner: Berhane, Adolf D.

Filed: 2/27/2004

Art Unit: 2838

For: EFFICIENT FREQUENCY COMPENSATION FOR LINEAR VOLTAGE REGULATORS

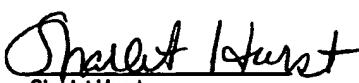
INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97 & 1.98

Assistant Commissioner for Patents
Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on

7-12-05


Sharlet Hurst

Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. A copy of the noted references is enclosed herewith.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668, and please credit any excess fees to such Deposit Account. Any further necessary extension of time is hereby requested.

Respectfully submitted,


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